## 3134

## BIPOLAR HALL-EFFECT SWITCH FOR HIGH-TEMPERATURE OPERATION



Pinning is shown viewed from branded side.

## ABSOLUTE MAXIMUM RATINGS at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$



This low-hysteresis bipolar Hall-effect switch is an extremely temperature-stable and stress-resistant sensor especially suited for operation over extended temperature ranges to $+150^{\circ} \mathrm{C}$. Superior high-temperature performance is made possible through a novel Schmitt trigger circuit that maintains operate and release point stability by compensating for temperature changes in the Hall element. Additionally, internal compensation provides magnetic switch points that become more sensitive with temperature, hence offsetting the usual degradation of the magnetic field with temperature. Its low hysteresis makes this device ideal for detecting small changes in magnetic field strength or for use with inexpensive magnets.

The device includes on a single silicon chip a voltage regulator, quadratic Hall-voltage generator, temperature compensation circuit, signal amplifier, Schmitt trigger, and a buffered open-collector output to sink up to 25 mA . The on-board regulator permits operation with supply voltages of 4.5 volts to 24 volts.

The first character of the part number suffix determines the device operating temperature range. Suffix 'E-' is for $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$, and suffix ' $\mathrm{L}-$ ' is $-40^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$. Two package styles provide a magnetically optimized package for most applications. Suffix '-LT' is a miniature SOT89/TO-243AA transistor package for surface-mount applications while suffix '-UA' is a three-lead ultra-mini-SIP.

## FEATURES

Superior Temperature Stability

- Operation From Unregulated Supply

Open-Collector 25 mA Output

- Reverse Battery Protection

Activate With Small, Commercially Available Permanent Magnets

- Solid-State Reliability
- Small Size
- Resistant to Physical Stress

Always order by complete part number, e.g., A3134ELT.


ELECTRICAL CHARACTERISTICS over operating temperature range, at $\mathrm{V}_{\mathrm{CC}}=12 \mathrm{~V}$.

| Characteristic | Symbol | Test Conditions | Limits |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min. | Typ. | Max. | Units |
| Supply Voltage | $V_{c c}$ | Operating | 4.5 | - | 24 | V |
| Output Saturation Voltage | $\mathrm{V}_{\text {OUT(SAT) }}$ | $\mathrm{I}_{\text {OUT }}=20 \mathrm{~mA}, \mathrm{~B}>\mathrm{B}_{\text {OP }}$ | - | 175 | 400 | mV |
| Output Leakage Current | $\mathrm{I}_{\text {OFF }}$ | $\mathrm{V}_{\text {OUT }}=24 \mathrm{~V}, \mathrm{~B}<\mathrm{B}_{\text {RP }}$ | - | 0.05 | 1.0 | $\mu \mathrm{A}$ |
| Supply Current | $\mathrm{I}_{\mathrm{cc}}$ | $\mathrm{B}<\mathrm{B}_{\mathrm{RP}}$ (Output OFF) | - | 3.2 | 9.0 | mA |
|  |  | $\mathrm{B}>\mathrm{B}_{\mathrm{Op}}$ (Output ON) | - | 5.0 | - | mA |
| Output Rise Time | $\mathrm{t}_{\mathrm{r}}$ | $\mathrm{R}_{\mathrm{L}}=820 \Omega, \mathrm{C}_{\mathrm{L}}=20 \mathrm{pF}$ | - | 100 | - | ns |
| Output Fall Time | $\mathrm{t}_{\mathrm{f}}$ | $\mathrm{R}_{\mathrm{L}}=820 \Omega, \mathrm{C}_{\mathrm{L}}=20 \mathrm{pF}$ | - | 100 | - | ns |

MAGNETIC CHARACTERISTICS over oper. temp. and oper. supply voltage ranges.

|  |  |  | Limits |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Characteristic | Symbol | Test Conditions | Min. | Typ. | Max. | Units |
| Operate Point | $\mathrm{B}_{\mathrm{OP}}$ |  | -40 | 8.5 | 60 | G |
| Release Point | $\mathrm{B}_{\mathrm{RP}}$ |  | -50 | -19 | 50 | G |
| Hysteresis | $\mathrm{B}_{\text {hvs }}$ |  | 5.0 | 27 | 55 | G |

NOTES: $\quad \mathrm{B}_{\mathrm{OP}}=$ operate point (output turns ON); $\mathrm{B}_{\mathrm{RP}}=$ release point (output turns OFF); $\mathrm{B}_{\text {hys }}=$ hysteresis $\left(\mathrm{B}_{\mathrm{OP}}-\mathrm{B}_{\mathrm{RP}}\right)$.
As used here, negative flux densities are defined as less than zero (algebraic convention).
Typical values are at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ and $\mathrm{V}_{\mathrm{CC}}=12 \mathrm{~V}$.
1 gauss (G) is exactly equal to 0.1 millitesla (mT).

| 3134 |
| ---: | ---: | ---: |
| LOW-HYSTERESIS |
| BIPOLAR HALL-EFFECT SWITCH |
| FOR HIGH-TEMP.OPERATION |

## TYPICAL OPERATING CHARACTERISTICS



## SENSOR LOCATIONS

( $\pm 0.005$ " $[0.13 \mathrm{~mm}]$ die placement)


## Suffix "UA"



Dwg. MH-011-4C

## APPLICATIONS INFORMATION

Extensive applications information for Hall-effect sensors is available in:

- Hall-Effect IC Applications Guide, Application Note 27701;
- Hall-Effect Devices: Soldering, Gluing, Potting, Encapsulating, and Lead

Forming, Application Note 27703.1;

- Soldering of Through-Hole Hall-Sensor Dervices, Application Note 27703; and
- Soldering of Surface-Mount Hall-Sensor Devices, Application Note 27703.2. All are provided in Allegro Electronic Data Book, AMS-702. or at www.allegromicro.com


## OPERATION

The output of these devices (pin 3) switches low when the magnetic field at the Hall sensor exceeds the operate point threshold $\left(\mathrm{B}_{\mathrm{OP}}\right)$. At this point, the output voltage is $\mathrm{V}_{\mathrm{OUT}(\mathrm{SAT})}$. When the magnetic field is reduced to below the release point $\left(\mathrm{B}_{\mathrm{RP}}\right)$ the device output goes high. Note especially that release can occur when the magnetic field is removed but to ensure release, a field reversal is required. The difference in the magnetic operate and release points is called the hysteresis ( $\mathrm{B}_{\text {hys }}$ ) of the device. This built-in hysteresis allows clean switching of the output even in the presence of external mechanical vibration and electrical noise.

Powering up the device in the absence of a magnetic field (less than $\mathrm{B}_{\mathrm{OP}}$ and higher than $\mathrm{B}_{\mathrm{RP}}$ ) will allow in indeterminate output state. The correct state is warranted after the first excursion beyond $\mathrm{B}_{\mathrm{OP}}$ or $\mathrm{B}_{\mathrm{RP}}$.

The products described herein are manufactured under one or more of the following U.S. patents: 5,045,920; 5,264,783; 5,442,283; 5,389,889; 5,581,179; 5,517,112; 5,619,137; 5,621,319; 5,650,719; 5,686,894; 5,694,038; 5,729,130; 5,917,320; and other patents pending.

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# 3134 <br> LOW-HYSTERESIS <br> BIPOLAR HALL-EFFECT SWITCH <br> FOR HIGH-TEMP. OPERATION 

## PACKAGE DESIGNATOR ‘LT’ <br> (SOT89/TO-243AA)

Dimensions in Inches
(for reference only)

Dwg. MA-012-3 in

Pads 1, 2, 3, and A - Standard SOT89 Layout
Pads 1, 2, 3, and B - Low-Stress Version
Pads 1, 2, and 3 only - Lowest Stress, But Not Self Aligning


Dimensions in Millimeters
(controlling dimensions)


Dwg. MA-009-3A in


Dwg. MA-009-3A mm


Pads 1, 2, 3, and A - Standard SOT89 Layout
Pads 1, 2, 3, and B - Low-Stress Version
Pads 1, 2, and 3 only - Lowest Stress, But Not Self Aligning

NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.
2. Supplied in bulk pack ( 500 pieces per bag) or add "TR" to part number for tape and reel.
3. Only low-temperature $\left(\leq 240^{\circ} \mathrm{C}\right)$ reflow-soldering techniques are recommended for SOT89 devices.

## PACKAGE DESIGNATOR 'UA’



NOTES: 1. Tolerances on package height and width represent allowable mold offsets. Dimensions given are measured at the widest point (parting line).
2. Exact body and lead configuration at vendor's option within limits shown.
3. Height does not include mold gate flash.
4. Recommended minimum PWB hole diameter to clear transition area is $0.035^{\prime \prime}(0.89 \mathrm{~mm})$.
5. Where no tolerance is specified, dimension is nominal.
6. Supplied in bulk pack ( 500 pieces per bag).

Dimensions in Millimeters
(for reference only)


NOTE: Lead-form dimensions are the nominals produced on the forming equipment. No dimensional tolerance is implied or guaranteed for bulk packaging ( 500 pieces per bag).

